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Dielectric characteristics of low-permittivity silicate using electron beam direct patterning for intermetal dielectric applications

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A direct patterning technology of low-permittivity silicate-based polymer is investigated with electron-beam lithography for multilevel interconnections. The smallest feature size of 60 nm for damascene lines can be directly patterned in the silicate film. In this direct patterning, dielectric regions exposed by electron beam are crosslinked and form desirable patterns, while the others are dissolvable in an aqueous solution containing 2.38% tetramethylammonium hydroxide. With an optimum condition of electron-beam lithography, the electron-beam-irradiated silicate exhibits superior dielectric properties than that of the furnace-cured silicate film, due to minimizing the break of Si–H bonds and moisture uptake. The explanation is in agreement with the analyses of Fourier transform infrared spectroscopy and thermal desorption spectroscopy. © 2003 American Institute of Physics. [DOI: 10.1063/1.1628401]

As feature sizes of integrated circuits reduce beyond 180 nm and enter the sub-100-nm nanoscale fields, signal propagation delay due to parasitic resistance and capacitance between the interconnect lines becomes the predominant component of the overall device signal delay.1 The crosstalk and power dissipation both also increase with the escalating parasitic capacitance. To address these problems, copper (Cu) damascene technology with low permittivity (low-k)2–5 intermetal dielectrics (IMD) has been developing for multilevel interconnects. It is necessary to reduce fabrication procedures for the interconnect architecture, especially etch-stop layer, photoresist, and dry-etching processes for damascene technology. Moreover, it is worth noticing that low-k layer, photoresist, and dry-etching processes for damascene procedures for the interconnect architecture, especially etch-stop level interconnects. It is necessary to reduce fabrication processes and to avoid the damage from photoresist stripping during Cu damascene manufacture.8 Although the major drawback of EB lithography is the very small throughput compared to current optical steppers, new ideas on masks and scanning strategies have been developing to resolve the throughput limitation on EB system.9–10 In this direct patterning, it was found that several low-k dielectric materials were sensitive to EB exposure. One of them, hydrogen silsesquioxane (HSQ)11 was used as a negative tone electron beam resist,12 since HSQ did not show only high resolution with a sensitivity comparable to poly(methyl methacrylate) (PMMA) but also a very low edge roughness.13 For the EB resist applications, HSQ layers are always stripped after finishing pattern transfer; thereby, dielectric properties of the HSQ films exposed by EB irradiation are not main factors in the process considerations. On the contrary, for IMD applications, low-k dielectric characteristics such as leakage current and dielectric constant naturally need to be carefully taken into consideration.1 In this letter, direct patterning of low-k HSQ using EB lithography is proposed as a multilevel interconnect technology. Dielectric characteristics of EB-exposed HSQ films are also investigated for IMD applications.

A nonphotosensitive silicate-based polymer, hydrogen silsesquioxane, was used as an electron sensitive low-k material through this study. HSQ resins diluted in methylisobutyl ketone (MIBK) were spun onto 6-in.-p-type Si wafers at 2000 rpm for 20 s to form 500-nm-thick films. After hot baking at 150 °C on a hot plate for 1 min, EB exposure was carried out according to target pattern layouts by use of a Leica Weprint200 stepper. The EB energy was 40 kV with beam size 20 nm and the exposure doses were changed from 100 to 700 μC/cm². After EB exposure, the HSQ films were developed in an aqueous solution containing 2.38% tetramethylammonium hydroxide (TMAH) for 1 min and rinsed in deionized water for 1 min. For the IMD dielectric investigations, unpatterned HSQ films were fabricated with EB blankly irradiating on the post-baked HSQ films. Then, the HSQ films were immersed in aforementioned developer TMAH and deionized water. Furnace annealing was finally performed at temperatures between 300 and 350 °C for 60 min in N₂ ambient to remove the residual organic solvent and to enhance the dielectric properties of EB-exposed HSQ films. The control samples were also manufactured for comparison, according to a typical commercial recipe, by baking as-spun HSQ films at 150, 200, and 300 °C for 1 min, re-
spectively, and followed by thermal curing in a furnace at 400 °C for 60 min under N₂ atmosphere. The chemical structures of all the HSQ films were determined by Fourier transform infrared (FTIR Bio-Red QS300) spectrometry. Electrical measurements were conducted on metal insulator semiconductor (MIS) capacitors. Material analysis using thermal desorption spectroscopy (TDS) was carried out upon heating samples from room temperature to 600 °C at a heating rate of 20 °C/s in vacuum (10⁻⁵ Pa). In the TDS analysis, m/e (mass-to-charge ratio)=18 peak that is attributed to H₂O was monitored to evaluate dielectric stability of the EB-exposed HSQ films.

FTIR spectra of HSQ films with different doses of EB exposure are shown in Fig. 1(a). In the HSQ film, Si–O bending mode (cage-like vibration at 863 cm⁻¹), Si–O stretching modes (cage-like vibration at near 1130 cm⁻¹, network-like vibration at near 1070 cm⁻¹), and Si–H stretching mode (near 2260 cm⁻¹) are observed. The Si–H group makes the surface of HSQ film hydrophobic and prevents moisture uptake. In addition, the low-k properties of HSQ film can be achieved if the density of Si–H bonding is maintained at a high level. With EB exposure, it is found that the peaks of Si–O stretching modes significantly change. The EB irradiation provides the as-baked HSQ film energy to be crosslinked and transfer the HSQ from cage-like structure to a network-like one. It clearly shows the intensity of Si–O network mode growth at the expense of the intensity of Si–O cage mode with increasing EB exposure doses. Moreover, the intensity of Si–H stretching mode is slightly decreased with increasing EB exposure doses. This observation indicates the structure of HSQ film changes from a cage-like to a stable three-dimensional network structure via the breakage of Si–O cage bonds and Si–H bonds, and subsequently forming Si–O–Si network bonding. The chemical reactions by EB could be simply summarized as follows:

\[
\text{SiH}_2 \xrightarrow{e^-} \text{Si}^\bullet \xrightarrow{H_2O} \text{SiOH} \xrightarrow{H_2O} \text{SiOSi}^\bullet.
\]

However, an excess of Si–H bonding breakdown will lead to the generation of dangling bonds in HSQ film, resulting in degraded dielectric properties. The optimum EB exposure, in this work, was obtained with a minimum dose of 400 μC/cm². In comparison with the furnace-cured HSQ film, as shown in Fig. 1(b), the intensity of Si–H bonds in the HSQ film with EB dosage 400 μC/cm² is relatively high. This result implies the EB-exposed HSQ could have superior dielectric characteristics than that of the furnace-cured HSQ. Scanning electron microscopy (SEM) micrographs of line patterns are displayed in Fig. 2. Dielectric regions of the HSQ films exposed by EB are crosslinked, while the others without EB exposure maintain gel-like states and are dissolvable in the TMAH aqueous solution, further forming trench patterns. The trench patterns will be suitable for the subsequent fill of Cu damascene lines. Also, Fig. 2 shows a good image selection ratio for the developer TMAH. The dimension with 60 nm for line widths is obtained with an EB dose of 400 μC/cm² without using conventional photoresist and dry-etching techniques. Figure 3 shows electrical characteristics of the EB exposed and furnace-cured HSQ films. The inset of Fig. 3 compares the dielectric constant of both...
samples. As can be seen, the leakage current of HSQ film with furnace curing is higher than that of the EB-exposed HSQ film. This phenomenon could be attributed to the fact that typically used furnace curing above 375 °C leads to Si–H bonds being broken,\textsuperscript{14,15} generating dangling bonds in the HSQ film. It is also consistent with the FTIR spectra [Fig. 1(b)] that less Si–H bonds exist in the furnace-cured HSQ compared to the EB-exposed HSQ. These dangling bonds are electrically trap centers and prone to moisture uptake, easily forming leaky paths in HSQ film. In comparison with furnace curing, electron beam exposure uses high-energy electrons rather than heat, to initiate polymerization and crosslinking reactions in the silicate-based polymer. It thereby enhances specific physical and chemical properties, and minimizing the formation of dangling bonds. TDS analysis shown in Fig. 4 surely confirms our inference that the relatively high moisture content is monitored in the furnace-cured HSQ film. The first desorption peak at \( \approx 200 ^\circ \text{C} \) is attributed to moisture adsorbed at the silicate surface, and the second peak at 400–600 °C is attributed to moisture tightly bonded through Si–OH groups at the internal surface of silicate films.\textsuperscript{17} Therefore, the furnace-cured HSQ exhibits inferior dielectric characteristics to that of the EB-exposed HSQ due to relatively high moisture adsorption.

In conclusion, direct patterning of nonphotosensitive low-\( k \) HSQ has been achieved using EB lithography. In this work the smallest feature size of 60 nm for damascene lines has been demonstrated without using etch-stop layer, photoresist, and dry-etching technologies. The probability of low-\( k \) degradation can be thereby avoided during these pattern transfer processes. For intermetal dielectric applications, low-\( k \) characteristics of the HSQ film with EB exposure have been emphasized to explore. With an optimum condition of EB lithography, the EB-exposed HSQ film exhibits superior low-\( k \) properties than that of the furnace-cured HSQ, due to minimizing the break of Si–H bonds and moisture uptake.

Material analyses including FTIR and TDS have confirmed our explanation, certainly indicating that relatively high intensity of Si–H bonds and low content of moisture are present in the electron-beam-exposed silicate.

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\textsuperscript{1}International Technology Roadmap for Semiconductors (ITRS), Santa Clara, CA, November, 2001.