This work investigates the thermal stability and physical and barrier characteristics of two species of amorphous silicon carbide dielectric films: the nitrogen-containing α-SiCN with a dielectric constant of 4.9 and the nitrogen-free α-SiC film with a dielectric constant of 3.8. The time-dependent-dielectric-breakdown (TDD) lifetime of the Cu damascene metatilization structure is greatly improved by using an α-SiCN/α-SiC bilayer dielectric stack as the barrier layer. This improvement is attributed to the lower leakage current of α-SiC, absence of nitridation on the Cu surface, and better adhesion of α-SiC on Cu and organosilicate glass intermetal dielectric. Although the α-SiC film has a very low deposition rate, the α-SiCN/α-SiC bilayer dielectric is a favorable combination for the barrier layer because α-SiC can protect α-SiC from plasma attack, such as O₂ plasma attack during photoresist stripping and organosilicate plasma attack during organosilicate glass deposition.

As the device dimensions continuously shrink, the operation speed of the integrated circuits is dominated by the resistance-capacitance (RC) time constant of the interconnect system. In order to minimize signal propagation delay, it is inevitable to use low-dielectric-constant (low-k) materials as the inter- and intralayer dielectrics (ILD). While many low-k (k < 3) materials have been used as ILDs, silicon nitride with a high dielectric constant (k > 7) is still the primary candidate for the barrier required in copper damascene structures. Thus, it is desirable to replace silicon nitride by other new materials with a lower k. In this work, we first investigate the thermal stability and physical and barrier characteristics of two species of α-SiC:H low-k dielectric films (k < 5). This is followed by investigating the TDDB lifetime of a Cu damascene metatilization structure having a bilayer dielectric barrier made up of these two species of films.

### Experimental

Two species of PECVD amorphous silicon carbide with k-values less than 5 are investigated with respect to the thermal stability and physical and barrier characteristics. The silicon carbide films of α-SiCN and α-SiC, designated as SCN and SC, respectively, were deposited on p-type, (100)-oriented Si wafers to a thickness of 50 nm. In addition, an SCN/SC bilayer dielectric film, designated as SCB, was also prepared with a 5 nm SC film deposited on the Si substrate followed by a 45 nm SCN film deposited on top of it. A metal electrode (Cu or Al) was deposited on the silicon carbide films to construct the metal-insulator-semiconductor (MIS) capacitor structure. For the Cu-electrode MIS capacitors, a Cu layer of 200 nm thickness was sputter deposited on the silicon carbide films, followed by a reactive sputter deposition of a 50 nm thick TaN layer on the Cu surface for the purpose of preventing oxidation of the Cu electrode during the subsequent thermal process. Al electrode samples were also prepared by depositing 500 nm thick Al layers directly on the silicon carbide dielectric surfaces. For better electrical measurements, a 500 nm thick Al layer was also deposited on the back side of the Si substrate for all samples. The TaN/Cu-electrode MIS capacitors were then annealed at 400°C for 30 min in an N₂ ambient. This annealing step eliminates the plasma-induced damage during the sputter deposition of TaN/Cu-electrodes and also provides the driving force for Cu diffusion. Cu damascene structures were constructed for the TDDB reliability study. Figure 1 shows the comb 1/serpentine/comb 2 test structure and the cross-sectional transmission electron microscopy (TEM) micrograph of the Cu damascene structure employed in this study. A PECVD OSG (α-SiO₂:H) film was first deposited to serve as the intermetal dielectric (IMD). After patterning of 0.2/0.2 µm linewidth/pace trenches in the OSG dielectric, the damascene Cu features were electrochemically deposited on a 30 nm thick TaN liner barrier. Following the Cu chemical mechanical polish, either an SCN (50 nm) single-layer dielectric or an SCN (45 nm)/SC (5 nm) bilayer dielectric stack (with the thin SC layer deposited first on the Cu/OSG surface), which is designated as SCB, was prepared as the barrier. The Cu damascene structure with an SC single-layer barrier was not used in this study because the SC dielectric has a very low deposition rate.
and is susceptible to plasma damage. Finally, the Cu damascene structure was passivated with an undoped silicate glass capping layer of 400 nm thickness.

An HP4145B semiconductor parameter analyzer was used to measure the dielectric leakage current and provide bias for the bias-temperature-stress (BTS) test. The dielectric film thickness and refractive index were measured using an N&K analyzer at 633 nm wavelength. Fourier transform infrared spectroscopy (FTIR) was used to analyze the chemical bonding of the dielectrics. The adhesion of films was measured using a four-point bending technique. X-ray photoelectron spectroscopy (XPS) was used to analyze the elemental composition of the Cu surface. The $k$-value of the dielectrics was determined by the maximum capacitance of the Al-gated MIS capacitors measured at 1 MHz using a Keithley Package 82 system.

### Results and Discussion

Table I shows the deposition gas, deposition temperature, deposition rate, and the refractive index and dielectric constant for the two species of PECVD amorphous silicon carbide investigated in this work. Notably, the deposition rate of SC, which contains no nitrogen at all, is much slower than that of SCN. Figure 2 shows the film thickness shrinkage as well as dielectric constant as a function of annealing temperature for three samples of PECVD $\alpha$-SiC:H dielectrics.

Figure 2. (a) Thickness shrinkage and (b) dielectric constant vs. annealing temperature for three samples of PECVD $\alpha$-SiC:H dielectrics.

Figure 3. FTIR spectra for three samples of PECVD $\alpha$-SiC:H dielectrics (a) SCN, (b) SC, and (c) SCB.

Figure 4. Current density vs. electric field, measured at various temperatures, for (a) Al/SCN/Si, (b) Al/SC/Si, and (c) Al/SCB/Si MIS capacitors.

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close to that of the SCN film. The substantial increase in thickness shrinkage and dielectric constant at temperatures above 500°C is attributed to the outgassing of the methyl group, resulting in a decrease and eventual disappearance of Si-CH₃, C-H, and Si-H peak heights, as shown in the FTIR spectra illustrated in Fig. 3.

The leakage current density of the Al/SCN/Si, Al/SC/Si, and Al/SCB/Si MIS capacitors measured at various temperatures is illustrated in Fig. 4. The TDDB was measured on the TaN/Cu-electrode SCN (50 nm), SC (50 nm), and SCB (45 nm SCN/5 nm SC) MIS samples at 200°C under a bias stress of 1 MV/cm. Figure 5 illustrates the leakage current transient during the BTS and the instantaneous leakage current density vs. applied electric field (in accumulation mode) before and after the BTS. All the samples remained stable under the BTS up to at least 15 h. This implies that both SCN and SC dielectric bulk films were capable of preventing Cu permeation.

Figure 6 and 7 illustrate the results of leakage current measurements and breakdown field statistics, respectively, for the Cu damascene metallization structures with a barrier layer of SCN (50 nm) as well as SCB (45 nm SCN/5 nm SC). The breakdown field is defined as the field strength such that the leakage current between comb 1/comb 2 (grounded) and serpentine (positive biased) exceeds 1 mA, and the histogram of the breakdown field was constructed from the data obtained by measuring 29 samples. The Cu damascene structure with an SCB bilayer barrier exhibits a much lower leakage current and higher breakdown field than that with an SCN single-layer barrier. Figure 8 shows the TDDB measurement at 200°C under a bias stress of 2 MV/cm for the Cu damascene structure with different barrier of SCN and SCB layer, and Fig. 9 shows the measured time-to-breakdown vs. BTS stress field. The damascene structure with an SCB bilayer barrier has a higher value of TDDB lifetime than the structure using an SCN single-layer barrier of the same thickness. All these observations are attributed to the lower leakage current of SC dielectric film (Fig. 4), because both dielectric films were capable of preventing Cu permeation (Fig. 5). In addition, the adhesion strength of SC/Cu and SC/OSG interfaces is superior to that of SCN/Cu and SCN/OSG interfaces, as shown in Table II. XPS analysis shows that there is a nitridation layer of CuNₓ at the SCN/Cu interface but not at the SC/Cu interface (not shown). The ionized Cu atoms of CuNₓ compound have a lower activation energy for diffusion, leading to the degradation of the TDDB reliability of Cu damascene structure with an SCN single-layer barrier. Because the SC dielectric has a very low deposition rate, we believe that the SCN/SC bilayer dielectric is a favorable combination for the barrier.
because the nitrogen-containing SCN film with the SiN_{x} compound can protect the SC film from plasma attack, such as O_{2} plasma attack during photoresist stripping\textsuperscript{10} and organosilicate plasma attack during OSG deposition.

**Conclusion**

The TDDB lifetime of Cu damascene metallization structure is greatly improved by using an \(\alpha\)-SiCN/\(\alpha\)-SiC bilayer dielectric stack as the barrier layer. This improvement is attributed to the lower leakage current of \(\alpha\)-SiC, absence of nitridation on Cu surface, and better adhesion of \(\alpha\)-SiC on Cu and OSG IMD. Although the \(\alpha\)-SiC film has a very low deposition rate, the \(\alpha\)-SiCN/\(\alpha\)-SiC bilayer dielectric is a favorable combination for the barrier layer because \(\alpha\)-SiCN can protect \(\alpha\)-SiC from plasma attack, such as O\textsubscript{2} plasma attack during photoresist stripping and organosilicate plasma attack during OSG deposition.

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