The effect of anodizing temperature on anodic oxide formed on pure Al thin films

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Abstract

The structure and properties of anodic Al\textsubscript{2}O\textsubscript{3} films formed at different anodizing temperatures from thin Al films deposited by magnetron sputtering on oxidized Si wafers have been studied by transmission electron microscopy, secondary ion mass spectrometry, and electrical measurements. The anodic oxide films formed at 8 °C and 25 °C are amorphous with excellent dielectric properties, leakage current lower than 500 nA at 7.5 MV cm\textsuperscript{-1} and no breakdown up to 7.5 MV cm\textsuperscript{-1}. Partially crystalline γ-Al\textsubscript{2}O\textsubscript{3} is grown at the upper half of the 58 °C oxide film. This film exhibits much inferior dielectric properties than the lower temperature films. Carbon is incorporated into the top portion of the oxide films from the electrolyte during anodizing and it has little effect on the crystalline oxide formation. Oxygen penetrates into the underlying Al layer during anodizing. The penetration increases with increasing anodizing temperature.

Keywords: Aluminium oxide; Anodic oxidation; Dielectric properties; Transmission electron microscopy

1. Introduction

The low resistivity and the well-established process technology of Al make it an attractive candidate for metallization in large size, high resolution, thin film transistors (TFT) liquid crystal display (LCD) application [1]. However, the propensity for hillock formation in thin film Al on Si substrates during heat processing has, until recently, prevented its application in TFT/LCD [2]. Hillocks generally form on Al films in order to relieve planar compressive stresses introduced either during deposition or subsequently by substrate-induced differential thermal expansion strains [3]. Recently, it was reported [1] that anodic aluminum oxide films can be used as a protective layer against hillock formation. Thin film transistors with Al gates and an anodic Al\textsubscript{2}O\textsubscript{3} + Si\textsubscript{3}N\textsubscript{4} double layer gate insulator have been successfully applied to the 10.4 in diagonal multicolor LCD display panel [1].

Two types of anodic aluminum oxide film can be formed on Al depending on the electrolyte used in anodization [4]. These are the thin, impervious barrier type and thick porous type films. For integrated circuit (IC) applications, the use of anodic Al\textsubscript{2}O\textsubscript{3} has been limited to the barrier films. Depending on the anodizing conditions, the anodic oxide film may be either amorphous, crystalline, or a mixture of both [5–7]. The degree of crystallinity of the anodic oxide generally increases with decreasing current density and increasing voltage [6, 7]. Crystalline films have a higher capacitance because of a larger dielectric constant [7], but exhibit an electrical instability [8] which is related to the presence of voids [9] and/or to the trapped oxygen [10]. For integrated circuit applications, the characteristics of the anodic aluminum oxide film are very important. In a previous paper we have studied the anodic oxide film formed on pre-annealed Al thin films containing 1% Si and 0.5% Cu [11]. In this paper the anodic oxide film formed on pure Al film without any pre-anneal is investigated. The effects of anodizing temperature on the structure, chemistry, and dielectric properties of the oxide films are examined in detail.
2. Experiment

The substrates for this study were 4 in diameter, p-type, (100), Si wafers. To simulate a typical glass substrate used for LCD, a 550 nm thermal oxide was first grown on the substrate and a 600 nm thick borophosphosilicate glass (BPSG) was subsequently deposited by chemical vapor deposition at 720 °C. A 300 nm thick pure Al film was then deposited by d.c. magnetron sputtering on top of the BPSG layer. Al₂O₃ was grown by anodizing the samples in AGW electrolyte [12], which is a mixture of 3% aqueous solution of tartaric acid and propylene glycol at a volume ratio of 2 to 8. The electrolyte temperature was kept at either 8, 25, or 58 °C in a constant-temperature water bath during anodizing. Stainless steel was used as the counter-electrode, and typically about 90% of the wafer was immersed in the electrolyte while the exposed one tenth was connected to the anode through a Cu clamp. A programmable Keithley 237 Current/Voltage Source Measure Unit interfaced to a PC capable to sourcing and measuring voltage or current simultaneously was used as the power supply and the data logger. Constant current mode (current density = 0.4 mA cm⁻²) was employed initially until the voltage reached 100 V; the anodizing was then automatically switched to constant-voltage mode. The total time of anodization was kept to 40 min, constant for each run. A 400 nm thick Al film was sputtered on the anodized wafers and metal/insulator/metal (MIM) capacitors were fabricated for the subsequent current-voltage (I-V) and capacitance-voltage (C-V) characterization using the Al layer as the top electrode. The MIM capacitor electrodes are 200 x 200 μm squares. The I-V measurements were carried out with a HP 4145B Semiconductor Parameter Analyzer and the high-frequency (1 MHz) capacitance measurement was performed using a Keithley 82 C-V system. The structures of the oxide films were studied by transmission electron microscopy (TEM). Both cross-sectional and planar TEM samples were prepared by ion milling in the usual fashion [13] and examined with a Philips CM20 microscope operating at 160 kV. Depth profiles of the chemical composition of the films were determined by secondary ion mass spectrometry (SIMS) with a Cameca IMS-4F instrument using Cs⁺ primary beam of 14.5 keV impact energy of 50 nA current.

3. Results and discussion

The variations of current and voltage with time during anodizing at different temperatures are shown in Fig. 1 and Fig. 2 respectively. In Fig. 1, the sudden drop of the anodic current from 30 mA signifies the onset of constant voltage mode (i.e. the voltage has reached 100 V). Apparently, the lower the electrolyte temperature the faster the anodizing voltage reaches 100 V. The 58 °C curve in Fig. 1 shows a peak at 2000 s, which is probably caused by a sporadic, localized dielectric breakdown during anodizing. The anodic current is eventually decreased to about 0.52 mA (0.007 mA cm⁻²), independent of temperature.

In Fig. 2, the initial rise of the curves corresponds to the stage where the anodization was conducted in constant current mode. The fact that the lower the temperature the higher the voltage in Fig. 2 during this initial period indicates that higher voltage is required for the lower-temperature electrolyte to reach the same current density level. Since this difference is observed from the very beginning of the anodizing when the conditions of the growth native oxide are the same for the three samples, it may be attributed to the decreased ionic...
mobility of the electrolyte at lower temperatures so that higher electric field strength is needed for a given current density. The rate of increase in voltage (defined as voltage growth rate) during the constant current period, which is the slope of the slanted lines in Fig. 2, varies slightly with the anodizing temperature, being 0.21 V s$^{-1}$ at 8°C and 25°C, but 0.16 V s$^{-1}$ at 58°C. This variation is presumably due to the difference in the microstructure of the anodic oxide films formed at different temperatures.

Fig. 3 shows cross-sectional TEM (XTEM) micrographs of the Al$_2$O$_3$ films anodized at 8, 25, and 58 °C respectively. The two lower-temperature oxide films are very similar: both are amorphous and practically pinhole-free (see Fig. 3(a) and (b)). The 58 °C oxide film shown in Fig. 3(c), however, is composed of two layers: an impervious amorphous layer at the bottom and a void-impregnated crystalline layer on the top. Fig. 4 shows the 58 °C films at a higher magnification, which shows clearly that the top layer is completely crystalline, and numerous patches of amorphous Al$_2$O$_3$ can be observed in it. In some locations, an example of which is marked by the arrow in Fig. 4, the Al$_2$O$_3$ is even completely amorphous throughout the entire layer. The voids associated with the crystalline oxide seem to concentrate near but not at the bottom of the crystalline region, and they appear as cracks in the film.

These cracks may be responsible for the localized breakdown observed in Fig. 1.

The nature of the crystalline phase in the 58 °C oxide film is better investigated with planar TEM specimens because a larger area is available for examination. Fig. 5(a) is a plane view bright-field image of the upper crystalline layer, and the corresponding dark-field image of the same area is shown in Fig. 5(b). Dispersed
small crystallites and amorphous patches are clearly discernible in these micrographs.

Diffraction patterns taken from this region (Fig. 6) were indexed using the substrate Si as the internal standard. The d-spacings corresponding to the rings in Fig. 6 are listed in the first column of Table 1 together with the description of their relative intensities in the second column. It is generally believed that anodic crystalline alumina is usually the $\gamma'$-phase alumina grown from nuclei originally existing in the form of $\gamma$-phase alumina [14]. Table 1 also gives the diffraction data for the $\gamma$-$\text{Al}_2\text{O}_3$ [15], $\gamma'$-$\text{Al}_2\text{O}_3$ [16] and pure Al [17]. It turns out that the d-spacings of many lattice planes in the three reference phases are very similar. For example, the d-spacing for the (200) of $\gamma$-$\text{Al}_2\text{O}_3$, the (400) of $\gamma'$-$\text{Al}_2\text{O}_3$, and the (200) of Al is 1.980, 1.977, and 2.024 Å respectively. This minor difference is well within the experimental error in the electron diffraction work, making the identification of the rings in Fig. 6 somewhat ambiguous. Careful comparison of the experimental data with three reference structures shows, however, that the ring pattern in Fig. 6 is most likely due to the $\gamma'$-$\text{Al}_2\text{O}_3$ phase for the following reasons.

(1) The experimental d-spacings and the corresponding intensities agree most favorably with the $\gamma'$-$\text{Al}_2\text{O}_3$ results of Stirland and Bicknell [16].

(2) Although the missing four reflections, (111), (220), (311) and (511), of the $\gamma$-$\text{Al}_2\text{O}_3$ phase in the experimental data are insufficient to rule out the presence of $\gamma$-$\text{Al}_2\text{O}_3$, the observed spacing of 1.205 Å definitely cannot be attributed to the $\gamma$-$\text{Al}_2\text{O}_3$ phase. These two facts combined strongly suggest that the crystalline oxide is unlikely to be $\gamma$-alumina.

(3) The observed d-spacings can also be matched reasonably well with the d-spacings of pure Al, so the question may be raised as to whether the crystalline material is actually the remnant Al layer on the amorphous alumina. But we believe this is not the case, because the diffraction pattern shown in Fig. 6 is totally different from the pattern taken from the Al layer in the same specimen (see Fig. 7). It should be pointed out that the diffraction pattern in Fig. 7 is in excellent agreement with the Al standard pattern, both in the d-spacings as well as in the relative intensities.

The mixed nature of the top crystalline layer is also confirmed by high-resolution TEM (HRTEM). Fig. 8 shows a HRTEM micrograph of the top layer in a 58°C oxide film, which reveals both the small $\gamma'$-$\text{Al}_2\text{O}_3$ crystallites and the patches of amorphous oxide. The size of the $\gamma'$-$\text{Al}_2\text{O}_3$ crystallites is about 50 nm or less. It is well known that the growth of anodic Al$_2$O$_3$ films is due to the mixed transport of both cations and anions, and that the growth occurs at both the Al/Al$_2$O$_3$ interface and the Al$_2$O$_3$/electrolyte interface [18, 19].

Crystalline anodic Al$_2$O$_3$ layers are known to form on Al metal pre-annealed in an oxidizing atmosphere at a temperature higher than 450°C. In such a case the crystalline layer is always formed near the center of an otherwise impervious amorphous layer. It has been convincingly demonstrated that the pre-annealing treatment causes crystalline $\gamma$-$\text{Al}_2\text{O}_3$ nuclei to form on the surface of the Al metal, and subsequent growth of the anodic Al$_2$O$_3$ film at the two interfaces buries these nuclei roughly in the center of an amorphous Al$_2$O$_3$ layer. The further growth of the crystalline nuclei is induced when a critical field strength is reached at a latter part of the anodization, and the growth proceeds by the crystallization of the nearby existing amorphous Al$_2$O$_3$ materials. Eventually a continuous crystalline Al$_2$O$_3$ layer would be formed in the center of an amorphous oxide film.

To prevent the formation of crystalline $\gamma$-$\text{Al}_2\text{O}_3$ nuclei on the Al films in the present study, we have purposely avoided the pre-anneal treatment and have, therefore, successfully prevented the crystalline Al$_2$O$_3$ layer formation at the two lower temperatures. (It should be pointed out that in a parallel study [11], crystalline oxide film was indeed formed in a sister sample which had gone through exactly the same treatment as the 25°C oxide film of the present study except that the sample was pre-annealed at 410°C for 30 min in air.) Although the crystalline Al$_2$O$_3$ layer still forms in the 58°C oxide film, it forms at the top of the oxide layer instead of being interposed in the center as in the pre-annealed samples. Being at the top of the anodic oxide layer indicates that they are not grown from the existing $\gamma$-$\text{Al}_2\text{O}_3$ nuclei which might have been present...
Comparison of diffraction spectra of $\gamma'$- and $\gamma$-alumina

<table>
<thead>
<tr>
<th>Experimental data from Fig. 6</th>
<th>$\gamma'$-alumina standard (Stirland and Bicknell) [16]</th>
<th>$\gamma$-alumina standard (JCPDS 10-425) [15]</th>
<th>Aluminum standard (JCPDS 4-0782) [17]</th>
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</thead>
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<tr>
<td>Ring #</td>
<td>$d_{\text{dis}}$ (Å)</td>
<td>$d_{\text{dis}}$ (Å)</td>
<td>$d_{\text{dis}}$ (Å)</td>
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<td></td>
<td>Intensity</td>
<td>(hkl)</td>
<td>Intensity</td>
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<td>S</td>
<td>(111)</td>
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<td>(444)</td>
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<tr>
<td>7</td>
<td>0.818</td>
<td>M</td>
<td>(844)</td>
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</table>

VS, very strong; S, strong; M, medium; F, faint; VF, very faint; VVF, very very faint.

on the surface of the as-deposited Al films. Since the top surface of the oxide layer is a place where new oxide continuously forms, the crystalline oxide layer formed there must be a growth product rather than a transformation product formed by the crystallization of the existing amorphous Al$_2$O$_3$ layer. The fact that crystalline Al$_2$O$_3$ forms only in the 58 °C film but not in the 8 °C film at 25 °C films shows that it is due, primarily, to a thermal effect. It is likely that the heat generated during anodizing cannot be easily removed from the upper oxide layer adjacent to the hot electrolyte, making the temperature there so high which, coupled with the applied electric field, favors the crystalline Al$_2$O$_3$ formation. The bottom layer of the oxide film, on the other hand, is in contact with the Al layer which, in our experimental set-up, is exposed to the ambient air outside the electrolyte. Since Al is an excellent conductor, the heat generated at the bottom of the oxide layer can be easily dissipated, so the temperature at the Al/Al$_2$O$_3$ interface remains low enough to favor the amorphous Al$_2$O$_3$ growth.

Anodically formed crystalline Al$_2$O$_3$ films are often associated with voids and pores, and this observation has been reported by numerous investigators [5, 6, 14]. Three models have been proposed for their origin: (1) voids are produced by the volume contraction associ-

Fig. 7. Electron diffraction pattern taken from the polycrystalline Al film. Reflecting planes corresponding to the numbered rings are: 1-(111), 2-(200), 3-(220), 4-(311), 5-(222), 6-(400), 7-(331), 8-(420), 9-(422).

Fig. 8. High resolution TEM image of the crystalline Al$_2$O$_3$ layer. Both $\gamma'$-Al$_2$O$_3$ crystallites and amorphous patches are discernible.
Table 2
Dielectric properties of the anodic films

<table>
<thead>
<tr>
<th>Anodic temp. (°C)</th>
<th>Capacitance (pF)</th>
<th>Leakage current (nA)</th>
<th>Oxide thickness (nm)</th>
<th>Dielectric constant</th>
<th>Breakdown field (MV cm⁻¹)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Mean</td>
<td>s.d. b</td>
<td>Mean</td>
<td>s.d. b</td>
<td></td>
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<tr>
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<td>278.0</td>
<td>135</td>
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<td>58</td>
<td>25.56</td>
<td>1.656</td>
<td>1321.3</td>
<td>95.2</td>
<td>125</td>
</tr>
</tbody>
</table>

aMIM capacitor electrode area = 200 × 200 μm².

bStandard deviation.

ated with the amorphous-to-crystalline transition [5]: (2) voids are formed by the trapped oxygen atoms near the crystalline particle during anodizing [6]; and (3) voids are formed by vacancy condensation [20]. The presence of voids and pores is detrimental to the dielectric properties of the oxide films. Fig. 9 is a Frenkel–Poole plot of the leakage current per electric field versus the square root of the electric field for the three films anodized at 8 °C, 25 °C and 58 °C. The leakage currents of the Al₂O₃ films were found to increase with temperature, and were mainly dominated by Frenkel–Poole emission, which is due to field-enhanced thermal excitation of trapped electrons into the conduction band [21]. Table 2 summarizes the dielectric properties of the oxide films. Here, the leakage current is defined as the current at 100 V and the breakdown field is the electric field corresponding to the current of 1 μA. The oxide thickness was measured directly on XTEM micrographs, and the dielectric constant was calculated from the capacitance and thickness data. It is evident from Table 2 that the dielectric properties of the 8 °C and 25 °C films are comparable while the 58 °C film shows a slightly larger capacitance, comparable dielectric constant, and much larger leakage current and lower breakdown field. The dielectric constant of our anodic oxide films is in the range of 8.5–9.2, which is consistent with the published data of 8.2–10.2 [22]. The larger leakage current observed in the 58 °C film is possibly due to the voids and the crystalline grain boundary in the oxide layer. The dielectric properties of our anodic oxide films compare favorably with those of Al₂O₃ films prepared by other methods, such as reactive electron beam evaporation (dielectric constant $\varepsilon = 7.6–7.8$, breakdown field $E_B = 1$ MV cm⁻¹) [23], chemical vapor deposition ($\varepsilon = 7.1–9.2$, $E_B = 7.4$ MV cm⁻¹) [24], and oxygen plasma deposition ($\varepsilon = 7–8$, $E_B = 4–8$ MV cm⁻¹) [25].

The effect of anodizing temperature on the film chemistry can be evaluated by SIMS analysis. Figs. 10 and 11 show respectively the composition profiles of the films formed at 8 °C and 58 °C. The Al₂O₃/Al interface is marked roughly by the vertical dashed line in each figure. The most striking feature here is the carbon pick-up in the oxide film, which is shown by the much higher (about 1.5 order of magnitude) carbon intensity in the oxide film than in the underlying Al film. The carbon is apparently coming from the electrolyte, which consists of tartaric acid (H₂O₂C(CH₁OH)₂COOH) and

![Fig. 9. Current–voltage characteristics of the anodic oxide films formed at three different temperatures.](image)

![Fig. 10. SIMS profile of the Al₂O₃ films anodized at 8 °C.](image)
propylene glycol (CH$_3$-CHOH-CH$_2$OH). Note that in both figures the carbon pick-up does not extend all the way to the Al$_2$O$_3$/Al interface, similar to phosphorus pick-up with phosphate-based electrolyte [18, 26] or the boron pick-up with borate-based electrolyte [18] reported in the literature. Carbon incorporation from the electrolyte is evidently not affecting the structure of the oxide film because both films contain carbon and yet one is amorphous but the other is mixed (amorphous + crystalline) type.

Another interesting observation from the SIMS profiles is the oxygen tails in the Al layer. Comparing the oxygen profile with the Al profile in Figs. 10 and 11, it is clear that the oxygen has penetrated deeper into the underlying Al layer in the 58 °C film than in the 8 °C film. This is probably due to the increased oxygen transport rate with increasing anodizing temperature.

4. Conclusions

(1) The anodic oxide films formed at 8 °C and 25 °C are completely amorphous and pinhole free. The dielectric properties of the low-temperature films are good, with leakage current lower than 500 nA at 7.5 MV cm$^{-1}$, and no breakdown is detected up to 7.5 MV cm$^{-1}$.

(2) Crystalline γ'-Al$_2$O$_3$ is formed at the upper half of the oxide layer in the 58 °C film. This film exhibits inferior dielectric properties compared with the low-temperature, amorphous oxide films.

(3) Carbon is incorporated into the top portion of the oxide films from the electrolyte during anodizing, and it has little effect on the crystalline oxide formation.

(4) Oxygen penetrates into the underlying Al layer during anodizing. The penetration increases with increasing anodizing temperature.

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[17] Powder Diffraction File # 4-0787, International Center for Diffraction Data, 1601 Part Lane, Swarthmore, PA 19081, USA.